



PATENT  
Docket No. 2369/23

**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

APPLICANTS : K. NAKAZATO, et al.

SERIAL NO. : 09/362,200

FILED : July 28, 1999

FOR : MEMORY DEVICE

GROUP ART UNIT : 2815

EXAMINER : E. Ortiz

ASSISTANT COMMISSIONER  
FOR PATENTS  
Washington, DC 20231

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J. Miller  
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**RESPONSE TO OFFICE ACTION**

SIR:

In response to the Office Action dated October 10, 2001, the due date being extended by two months as can be seen on the attached Petition for Extension of Time, please amend the above-identified application as follows:

**IN THE CLAIMS:**

Please cancel claims 13 and 14.

Please amend claims 1, 3, 5, 15, 21, 34-35, and 45 as follows:

1. (Amended) A memory device comprising an electrode structure, a charge storage node and a lamination structure including an insulating film and a semiconductor film, the lamination structure being disposed between the electrode structure and the charge storing node;
- the lamination structure having an energy band profile that is changeable between a first configuration in which a barrier height of the energy band profile is high and a second configuration in which the barrier height of the energy band profile is low, an electric current

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02 FC:103